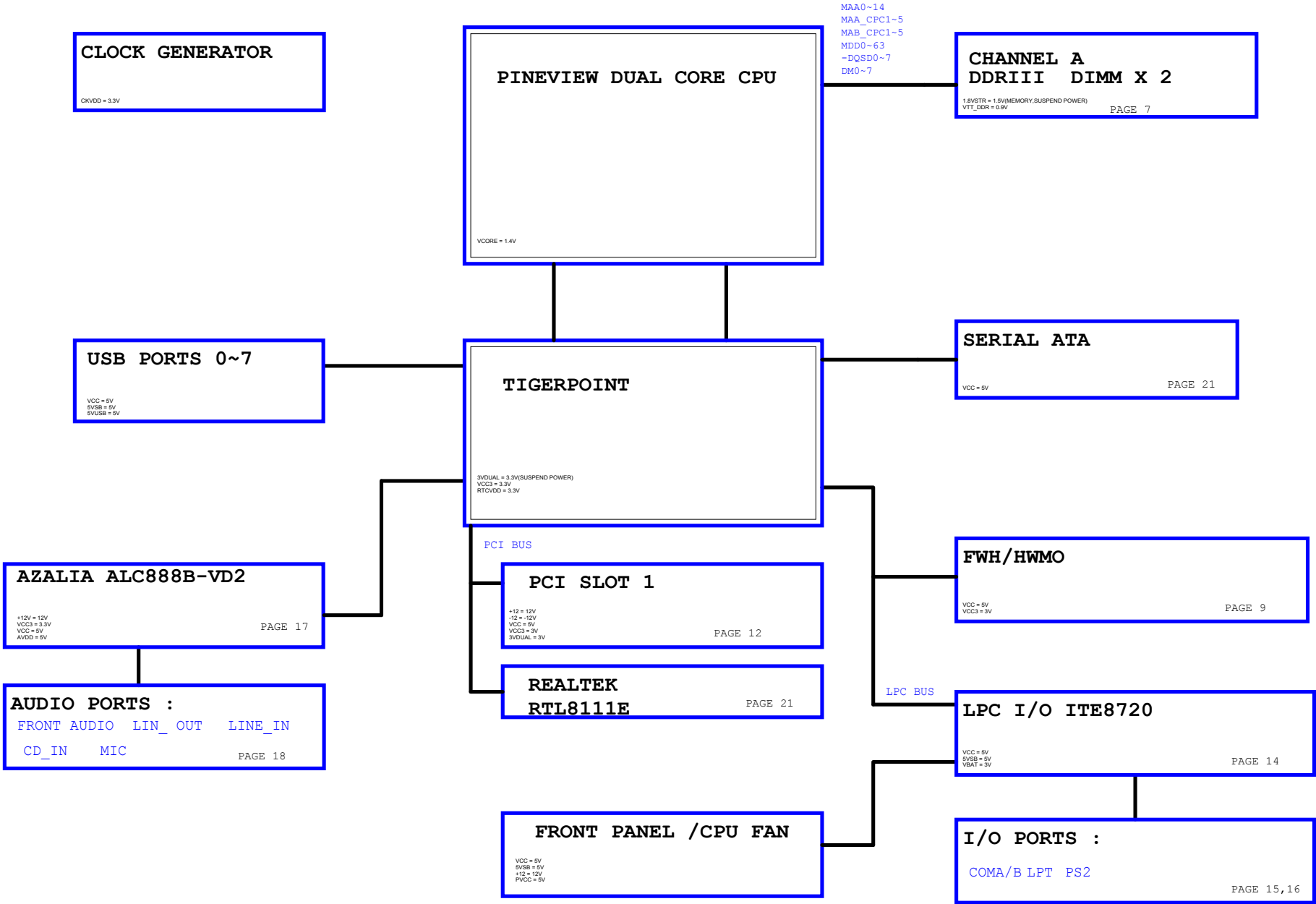


SHEET

TITLE

01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	PINEVIEW DC HOST,DDRII
05	PINEVIEW DC VGA,DMI
06	PINEVIEW DC PWR,GND
07	DDR3 CHANNEL A
08	TIGERPOINT PCI,DMI,LAN,USB
09	TIGERPOINT SATA,GPIO,CTRL,DUAL BIOS
10	TIGERPOINT PWR,GND
11	GB/CK505-OC CLOCK
12	PCI SLOT
13	F USB,LED
14	ITE 8720 GB
15	COM,LPT
16	CI,HWM,KB/MS
17	AZALIA ALC888B-VD2
18	REAR AUDIO JACK
19	DISCRETE POWER
20	ATX,OTHERS POWER
21	REALTEK RTL8111E
22	VCORE PWM ISL6314
23	FRONT PANEL
24	JMB363 2S,1P

BLOCK DIAGRAM



Version: 1.3

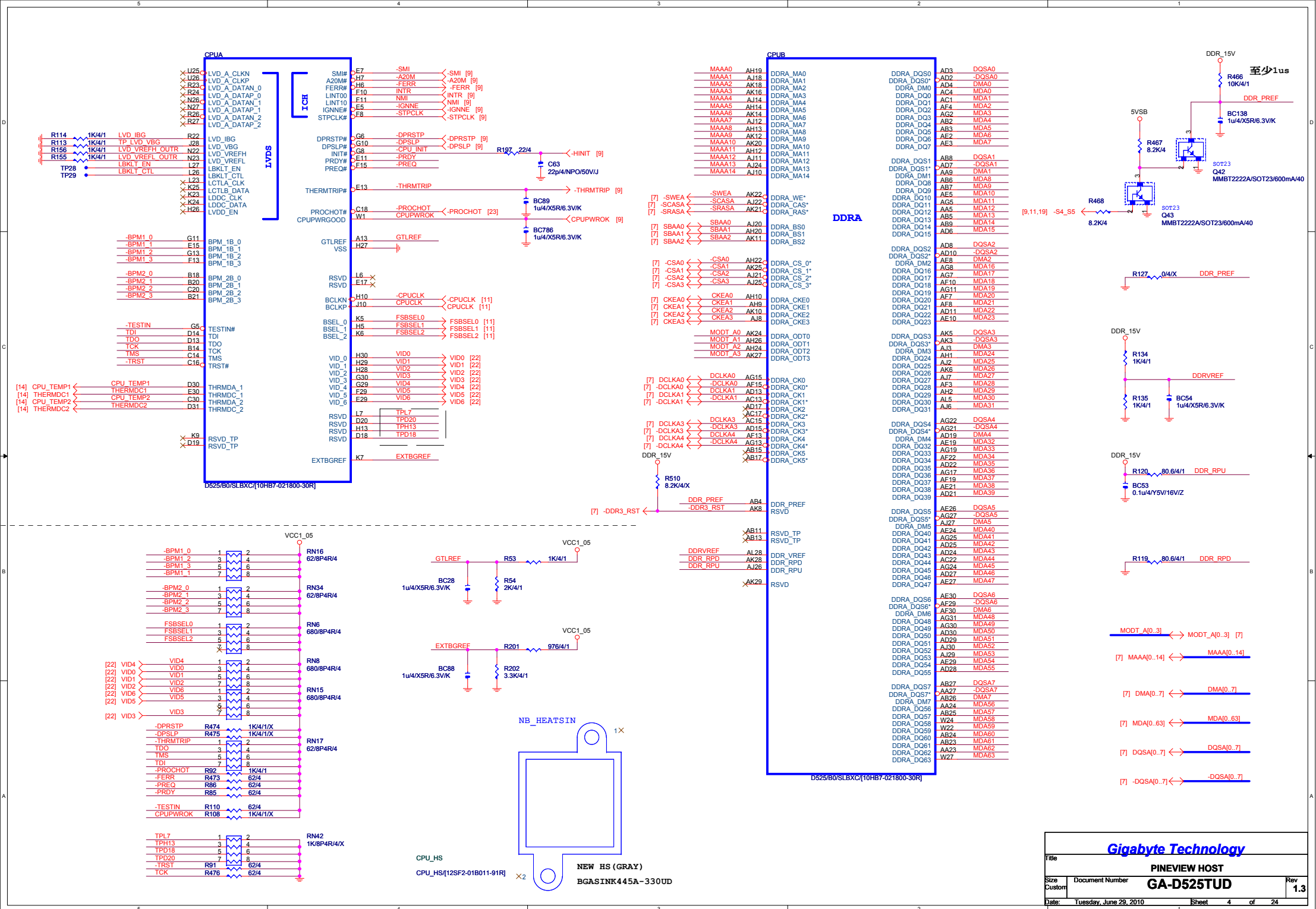
Component value change history

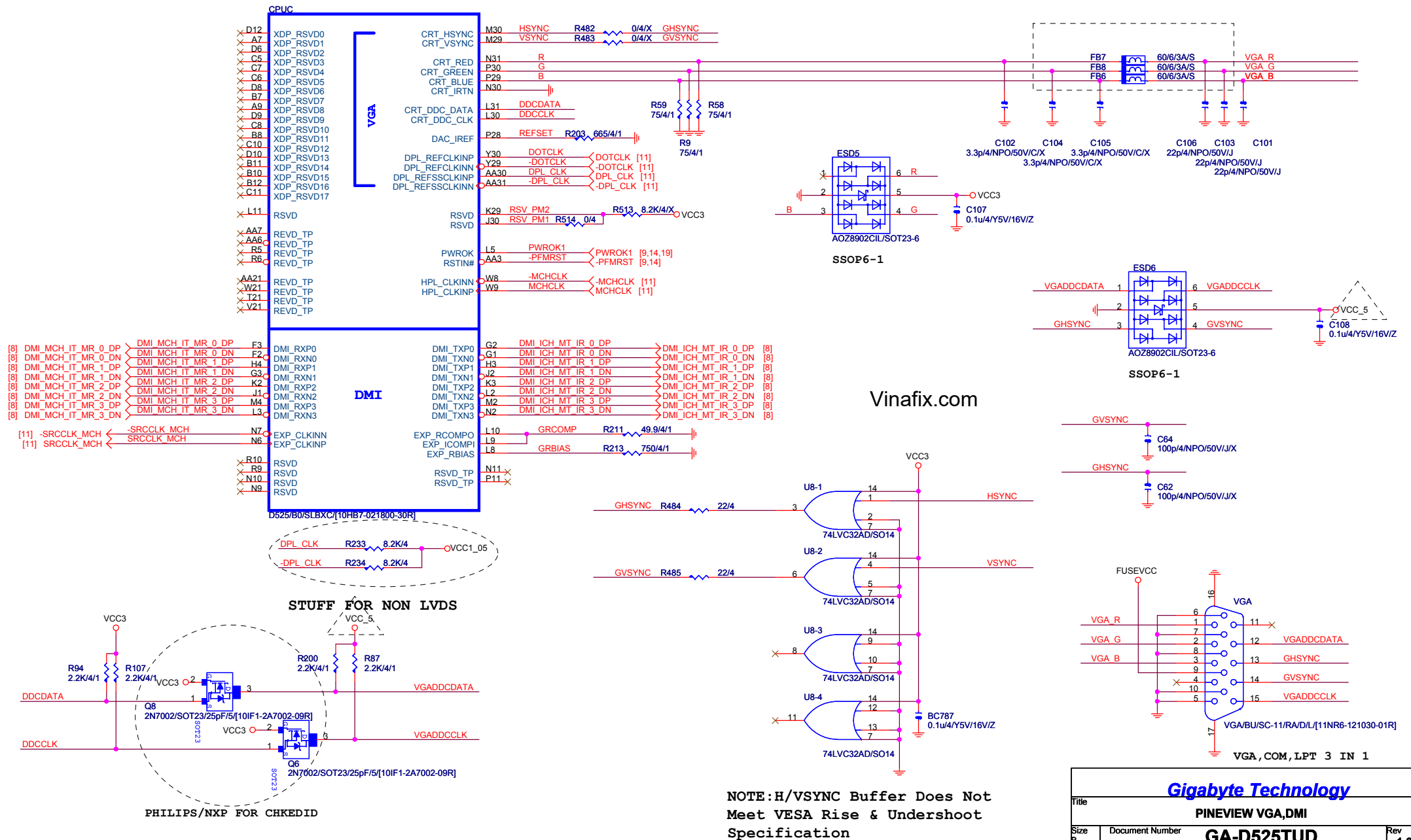
2010/07/15

[illegible]

Circuit or PCB layout change
for next version

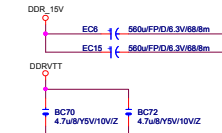
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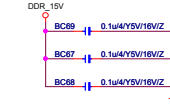


DDR TERMINATION CHANNEL A

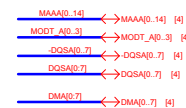
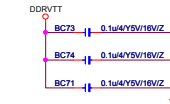
DDRVTT Decouple



DDR 15V Decouple

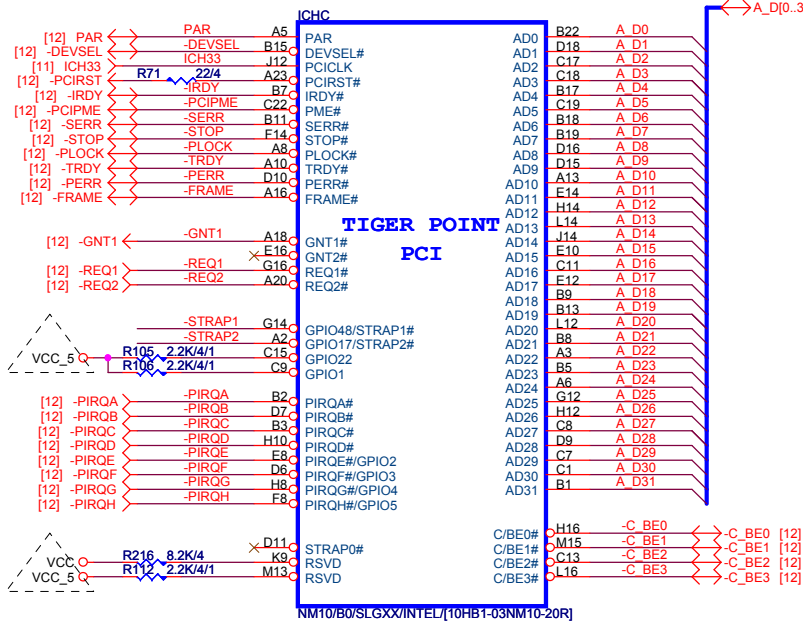


DDRVTT Decouple



Gigabyte Technology

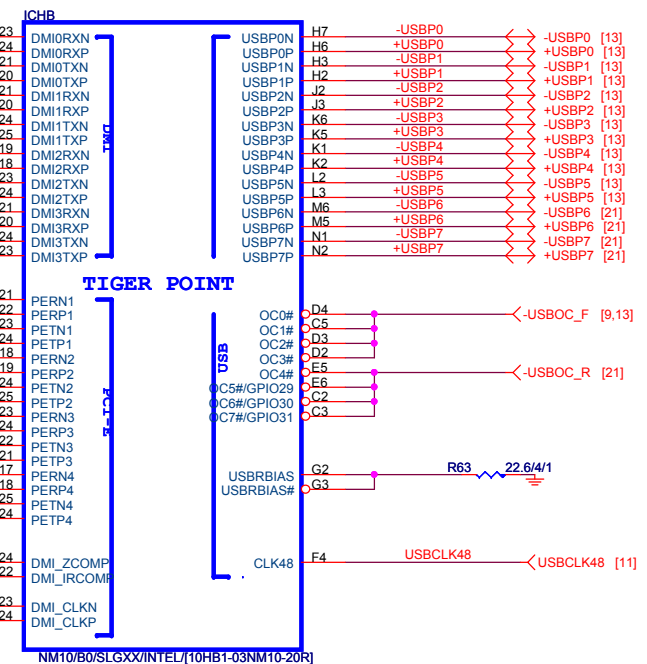
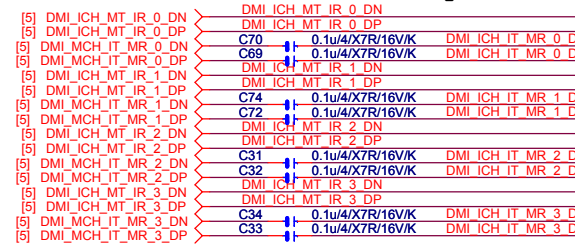
File	DDR3 CHANNEL A	Rev	1.3
Size	Document Number	GA-D525TUD	
Custom	Date	Friday, June 25, 2010	Sheet 7 of 24



← A_D[0..31] [12]

Close to ICH

as short as possible



STRAP TABLE	STRAP2	STRAP1
SPI	0	1
PCI	1	0
LPC	1	1

-STRAP1 R103 1K/4/1

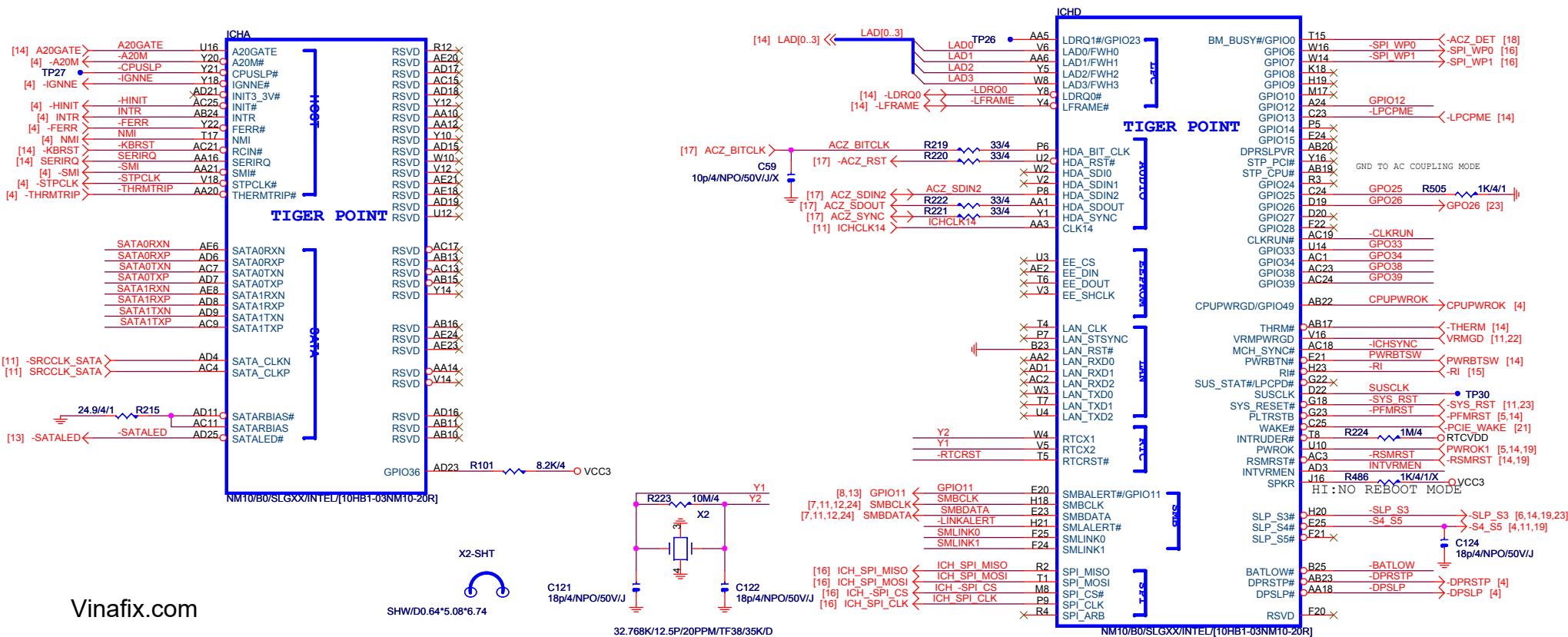
-STRAP2 R104 1K/4/1

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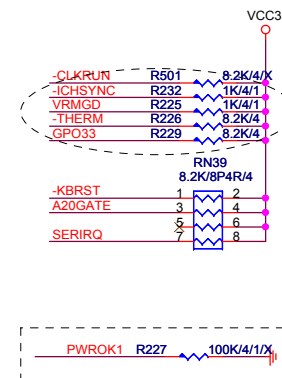
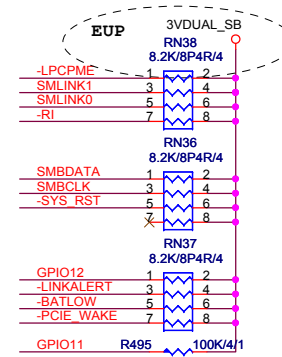
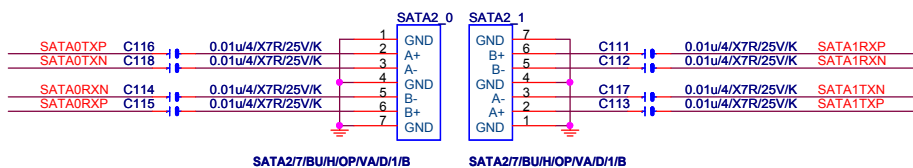
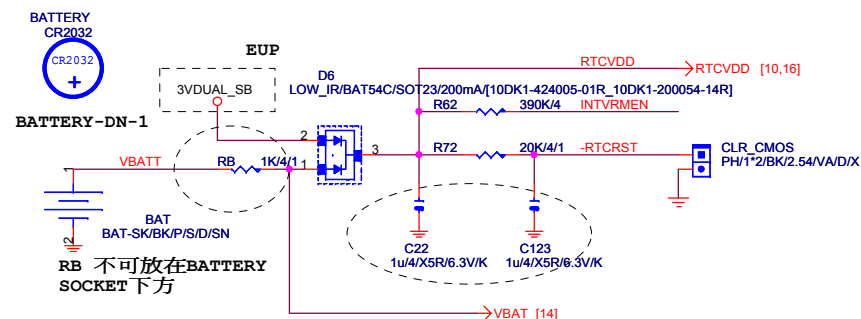
Title **TIGERPOINT-PCI E, DMI,LAN,USB**

Size B Document Number **GA-D525TUD** Rev **1.3**

Date: Friday, June 25, 2010 Sheet 8 of 24



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GPO25 (-RSMRST LATCH時不可為LOW, 否則不開機)

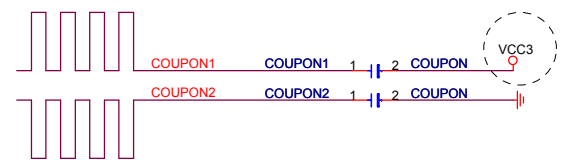
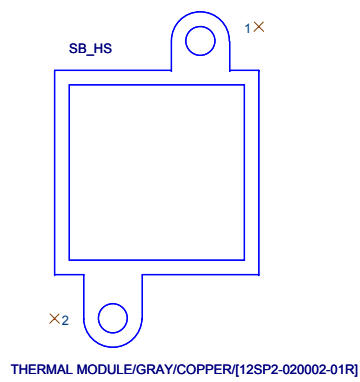
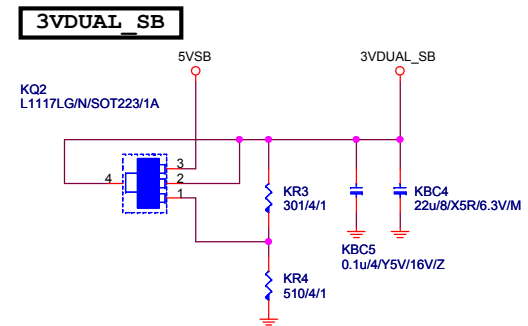
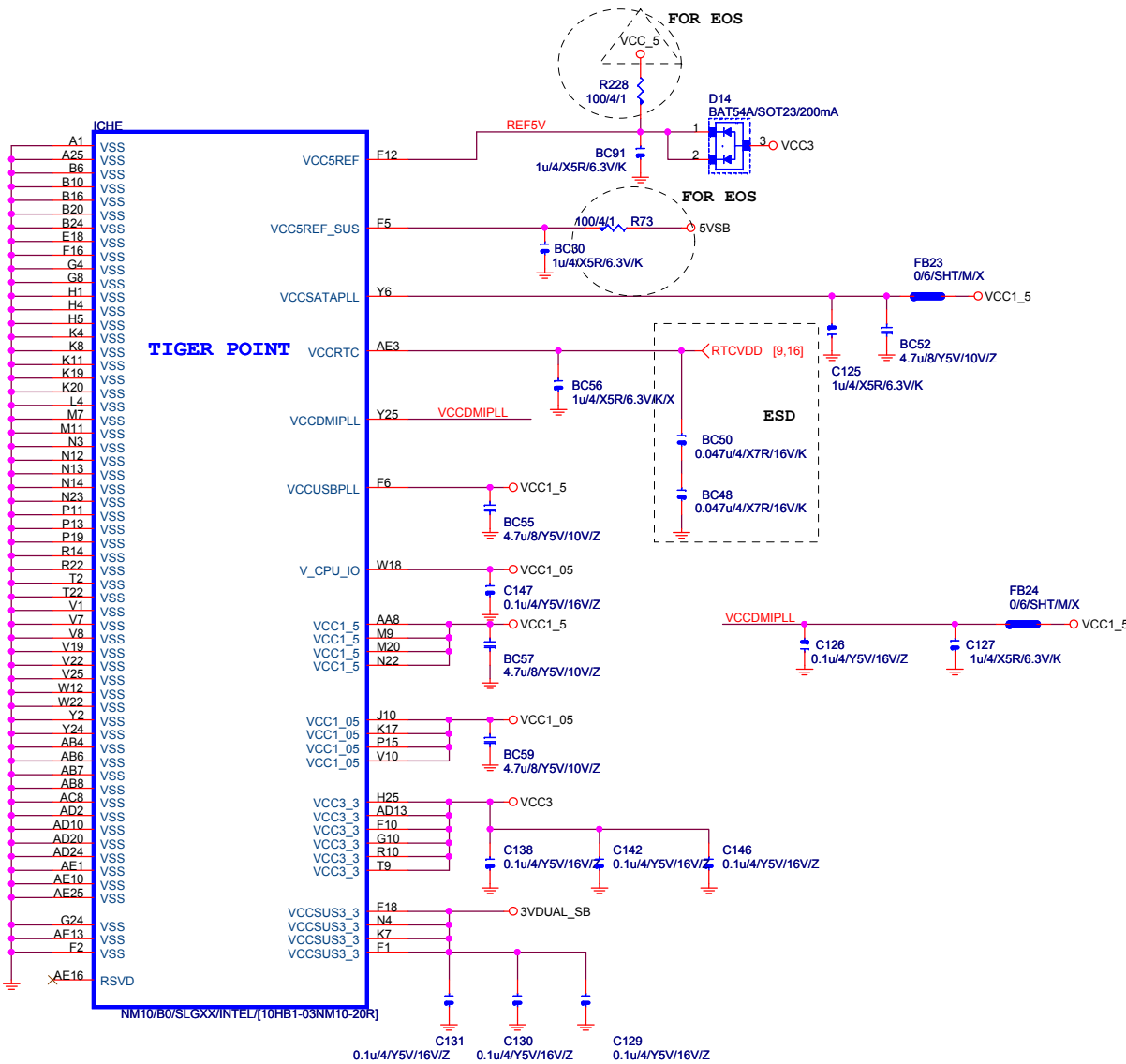
Gigabyte Technology

TIGERPOINT-SATA,GPIO,CTRL

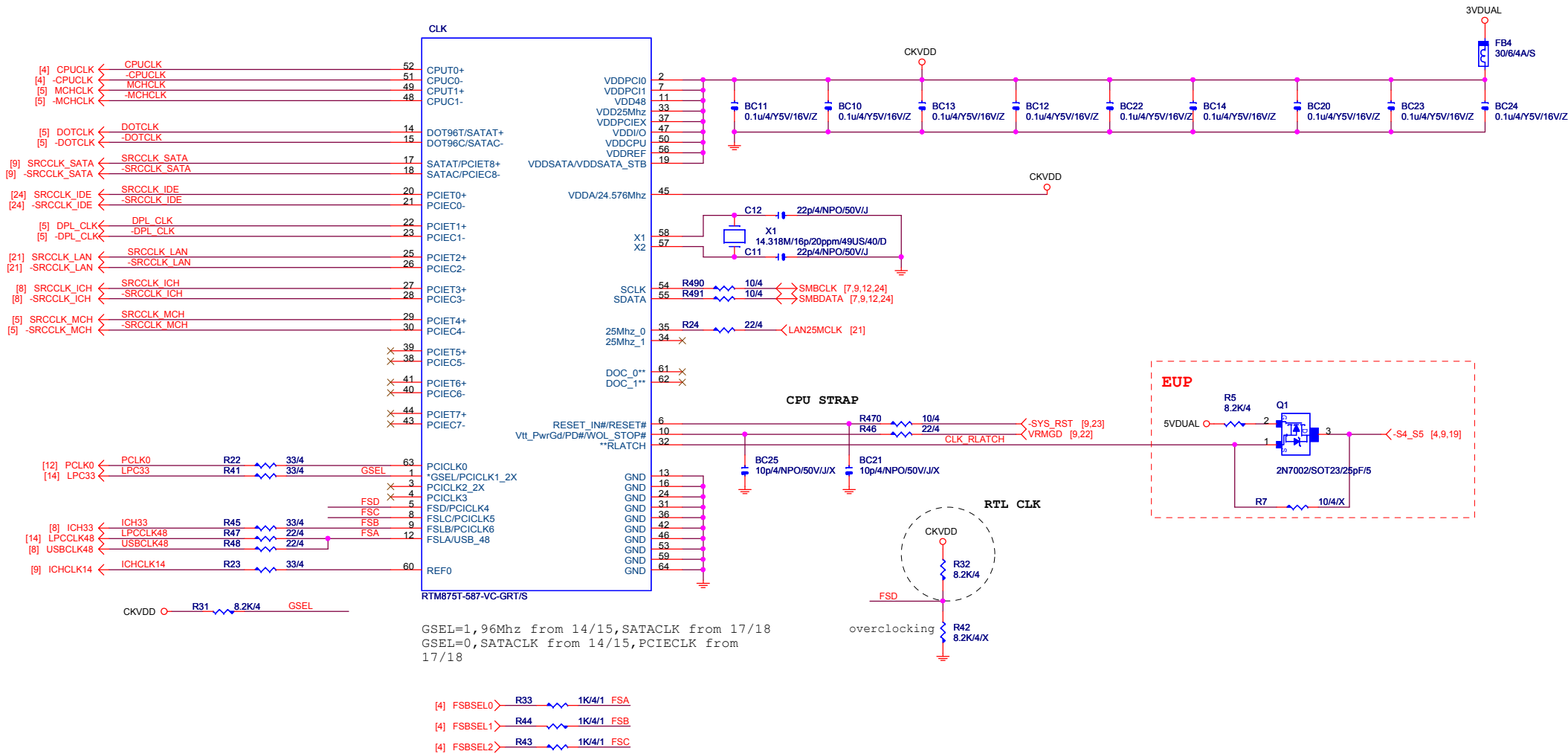
GA-D525TUD

Rev 1.3

Date: Friday, June 25, 2010 Sheet 9 of 24

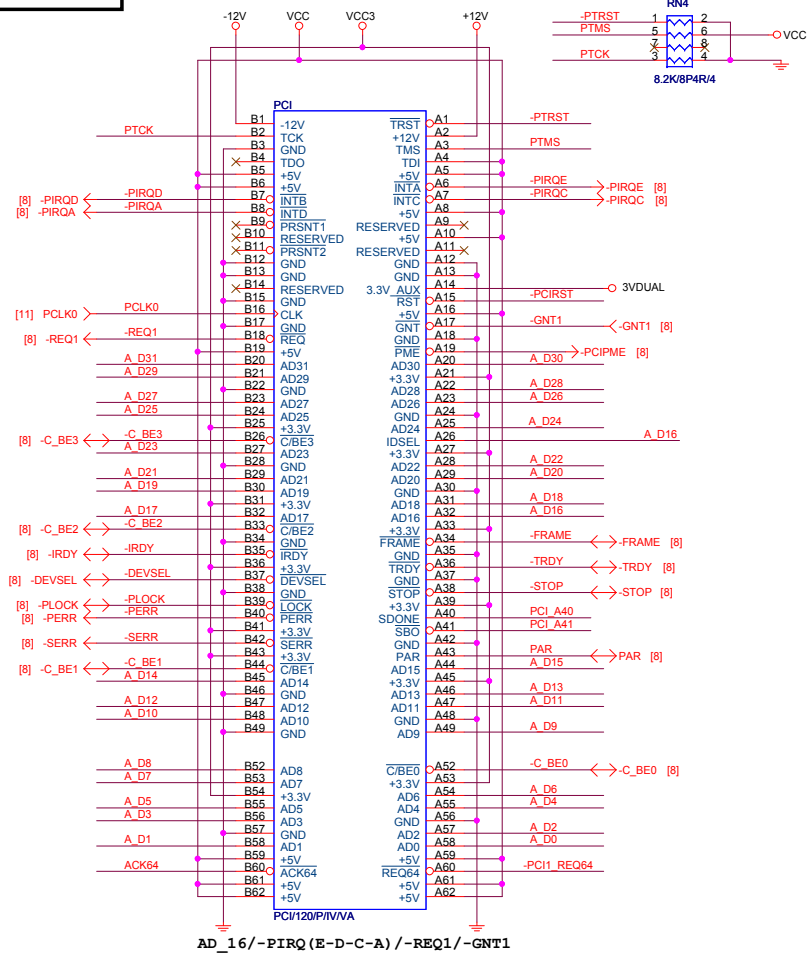


Gigabyte Technology			
Title			
TIGERPOINT-PWR,GND			
Size	Document Number	Rev	
B	GA-D525TUD	1.3	
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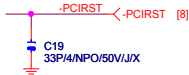
GSEL=1,96Mhz from 14/15,SATACLK from 17/18
GSEL=0,SATACLK from 14/15,PCIECLK from 17/18

PCI SLOT



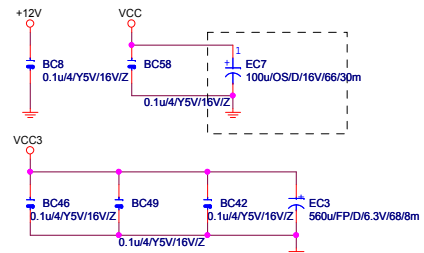
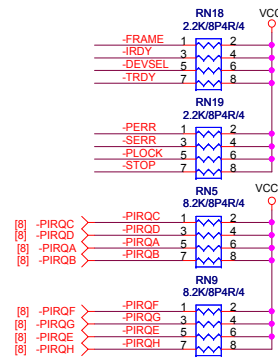
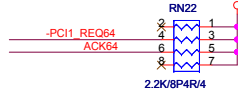
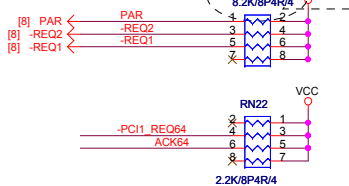
AD_16/-PIRQ (E-D-C-A) /-REQ1/-GNT1

[8] A_D[0..31] ↔ A_D[0..31]



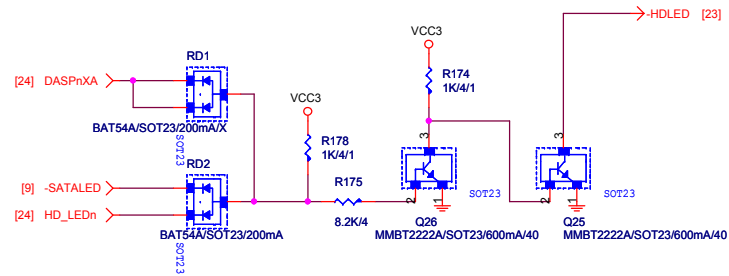
Place close to PCI1

[7,9,11,24] SMBCLK ↔ PCI_A40
[7,9,11,24] SMBDATA ↔ PCI_A41

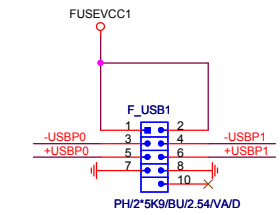
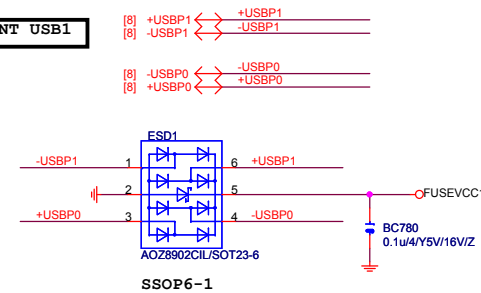


Gigabyte Technology			
PCI SLOT			
GA-D525TUD			
Size	Document Number	Rev	1.3
Custom			
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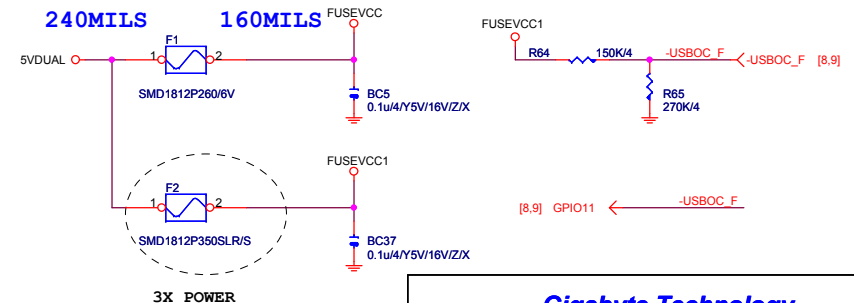
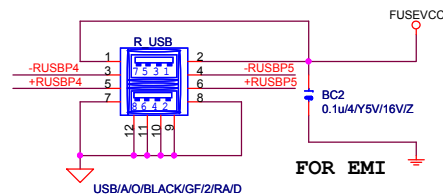
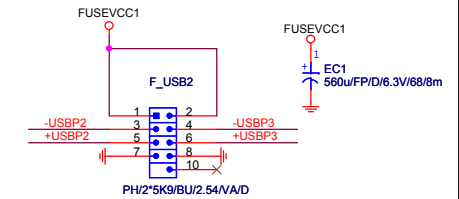
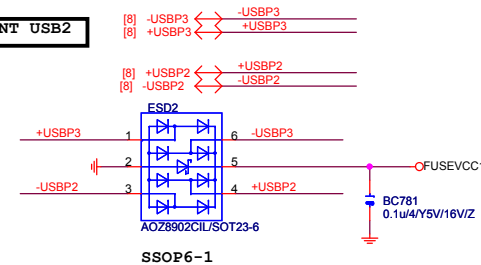
IDE/SATA LED



FRONT USB1



FRONT USB2



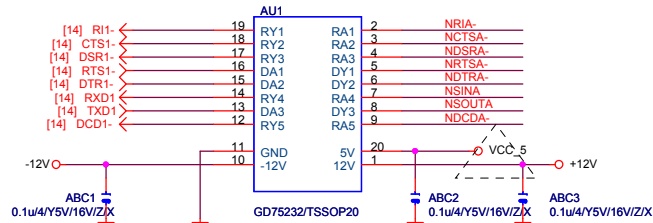
Gigabyte Technology		
Title		
IDE,FDD,F_USB		
GA-D525TUD		
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```
JP3 : Flashsegl_EN      JP5 : WDT_EN
JP4 : K8PWR_EN          1 : Disable WDT to rest PWROK
                        0 : Enable WDT to rest PWROK
```



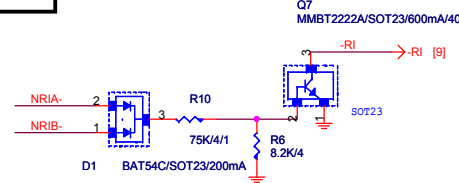
JP3	JP5	FAN_CTL_SEL
1	1	The default value of EC Index 15h/16h/17h is 40h.(50%)
1	0	The default value of EC Index 15h/16h/17h is 7Fh.(0%)
0	1	The default value of EC Index 15h/16h/17h is 00h.(100%)
0	0	The default value of EC Index 15h/16h/17h is 20h.(75%)

COM

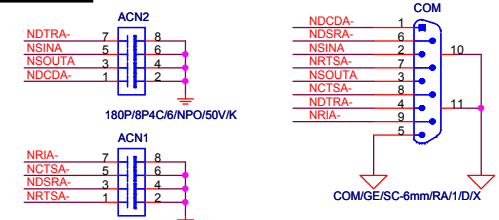


IC20TSSOP-1

RING IN

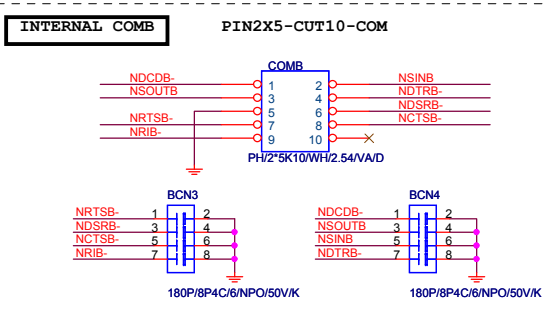
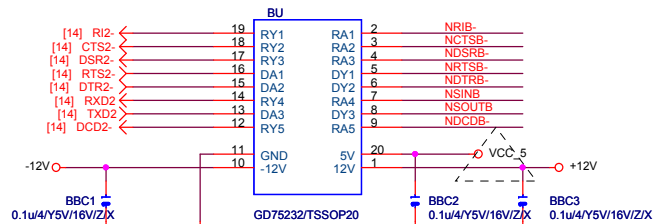


EXTERNAL COMA

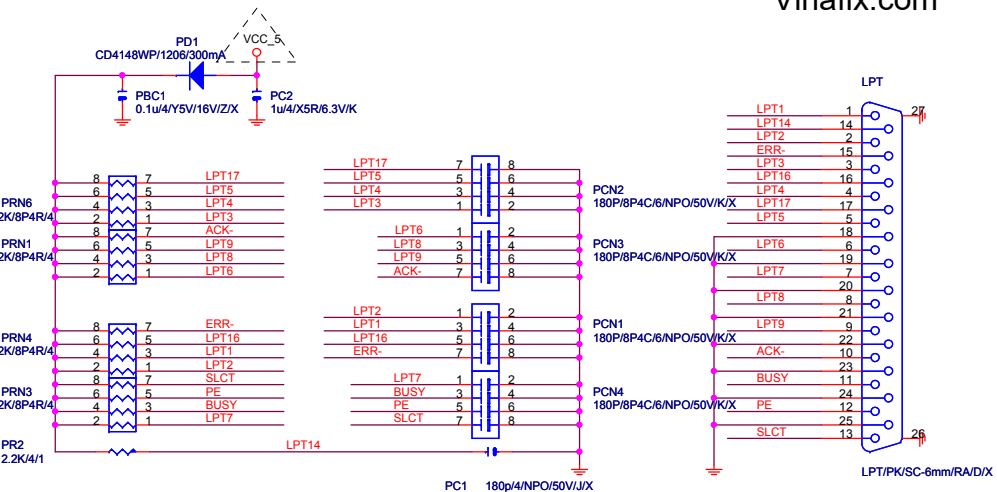
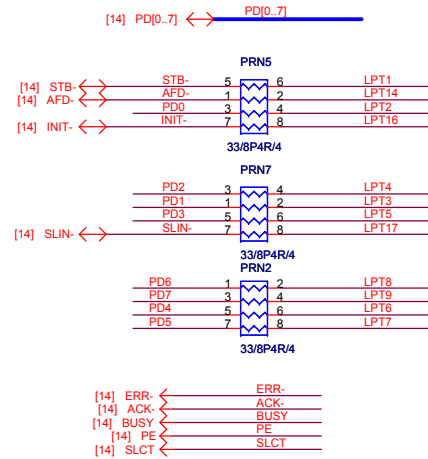


PLACE NEAR COM CONNECTOR

COMB

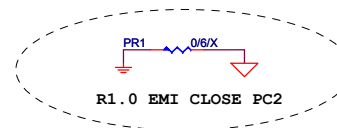


LPT PORT



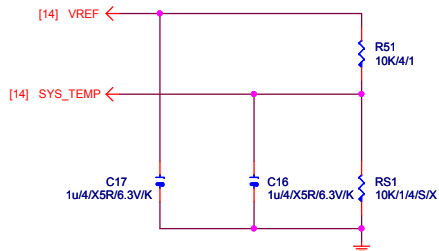
Vinafix.com

VGA, COM, LPT 3 IN 1

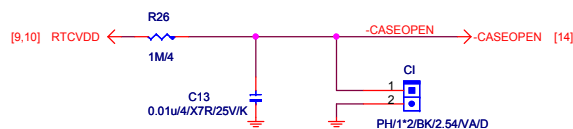


Gigabyte Technology			
Title			
COM & LPT PORT			
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TEMP H/W MONITOR

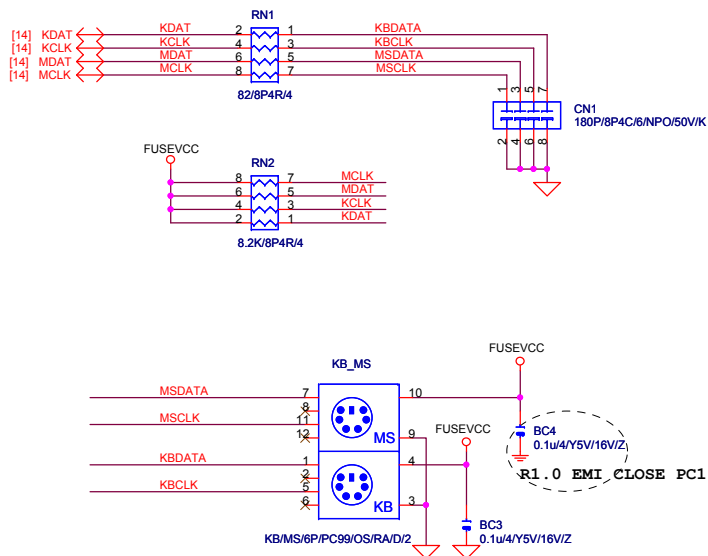


CASE OPEN

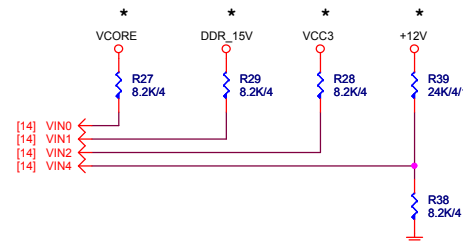


Case Open Circuits

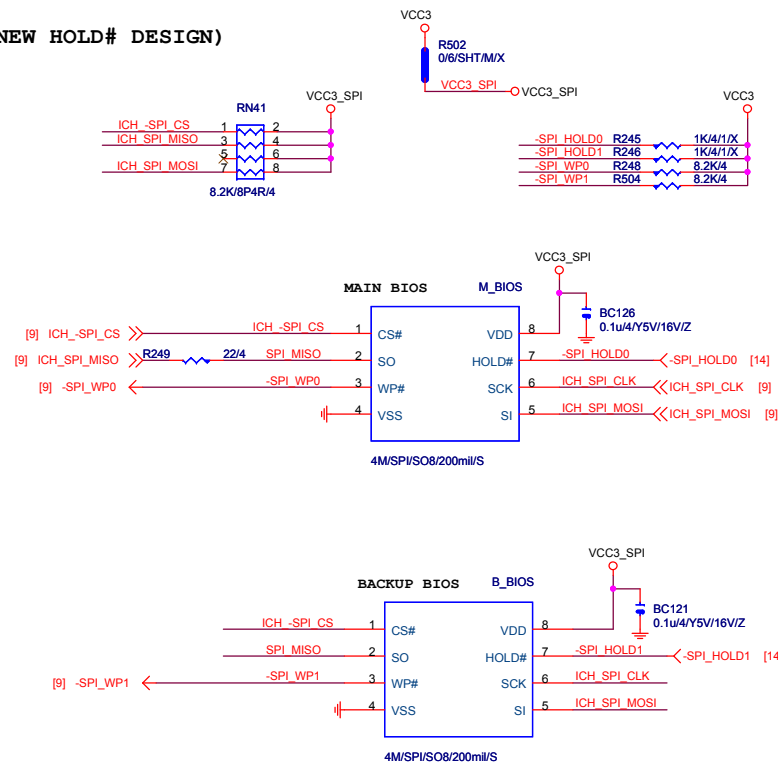
KB/MS

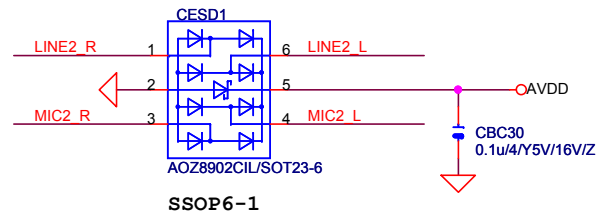
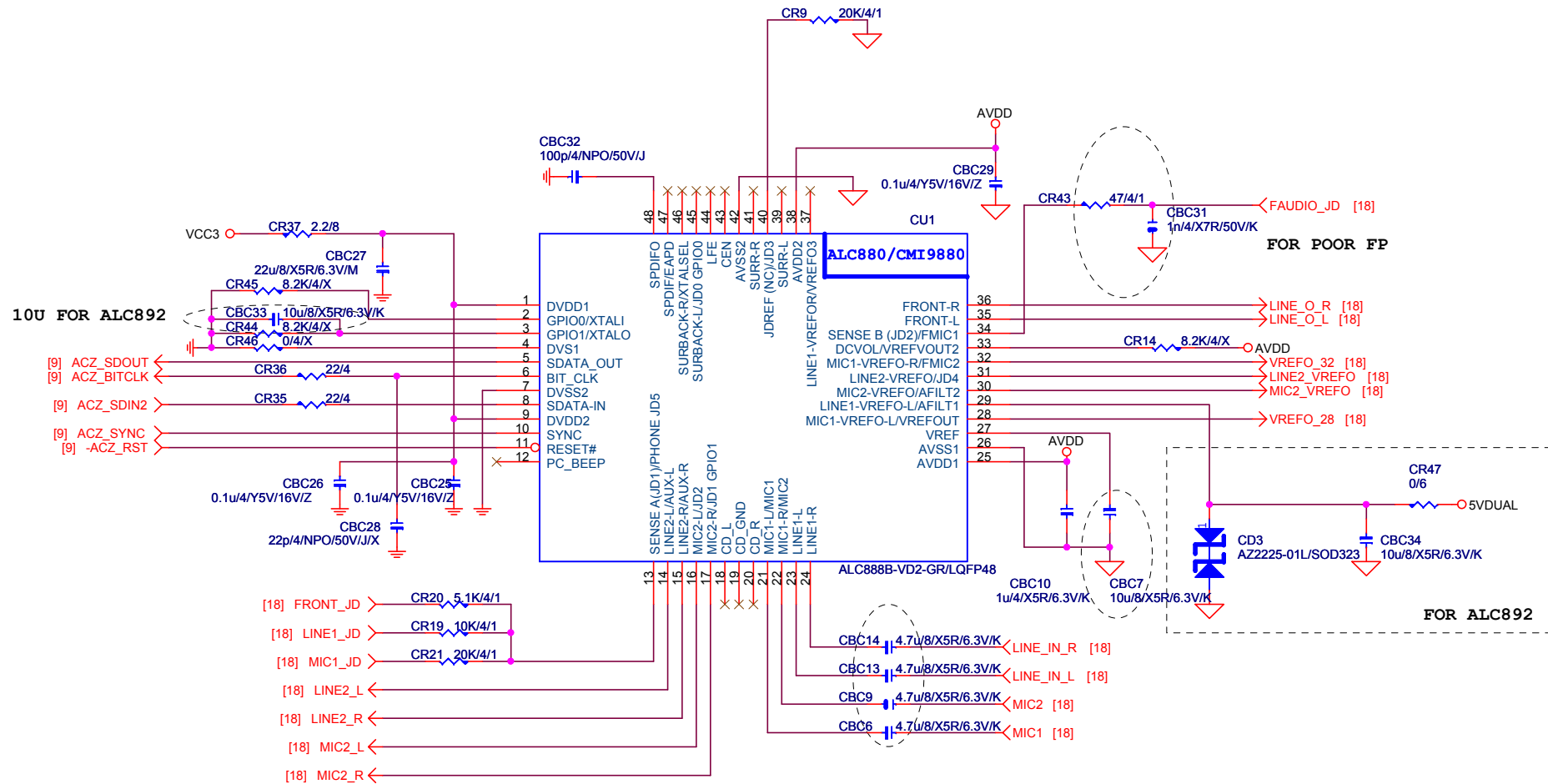


VOLTAGE-- H/W MONITOR



DUAL BIOS (NEW HOLD# DESIGN)

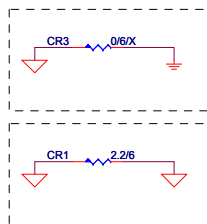
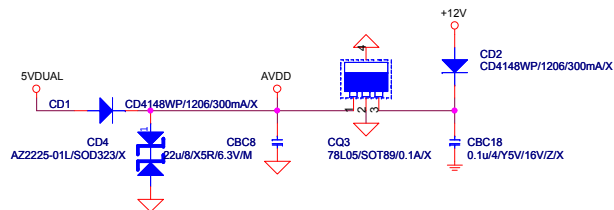




Gigabyte Technology

Title		
AZALIA ALC888B-VD2		
Size	Document Number	Rev
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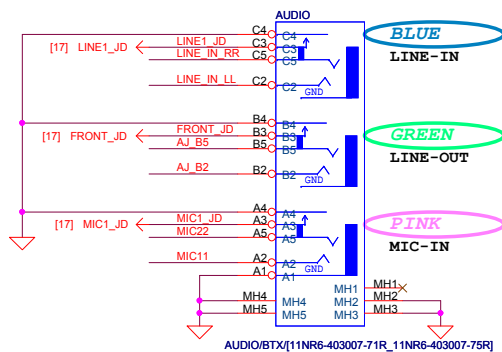
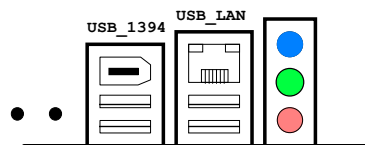
CODEC POWER/EMI PAD



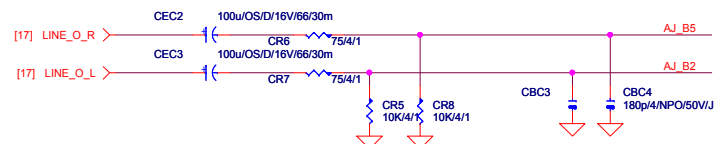
CD	IN
----	----

SPDIF

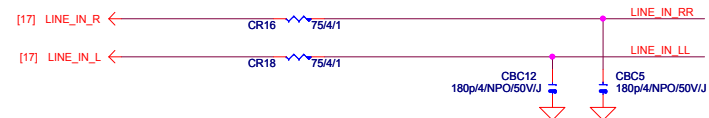
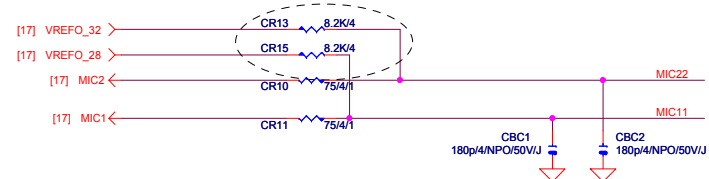
AZALIA JACK



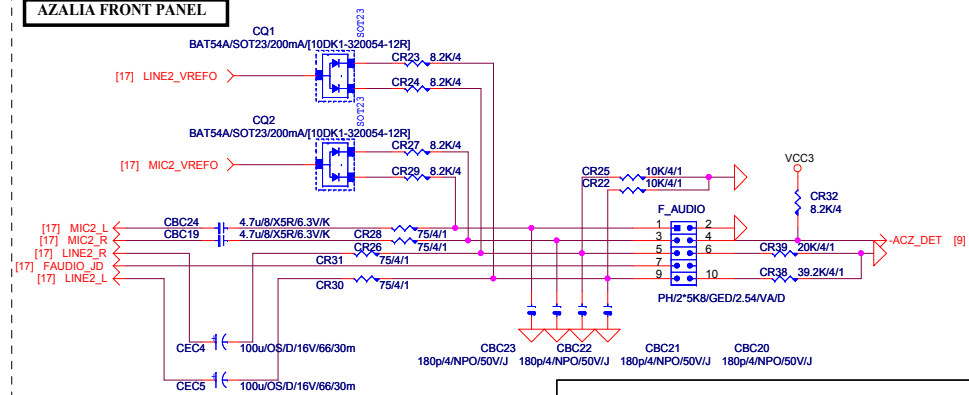
LINE-OUT



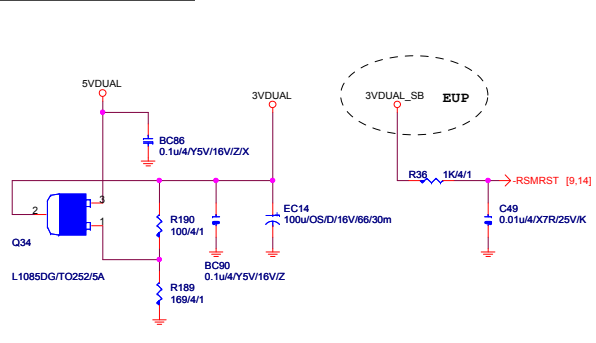
LINE-IN

**MIC-IN**

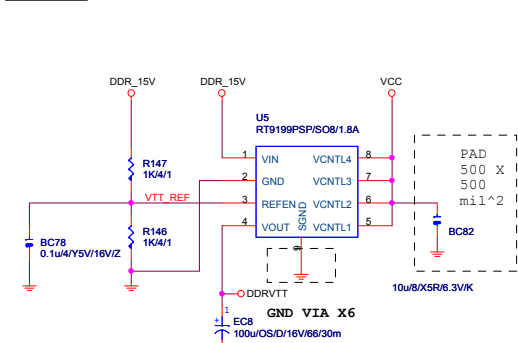
AZALIA FRONT PANEL



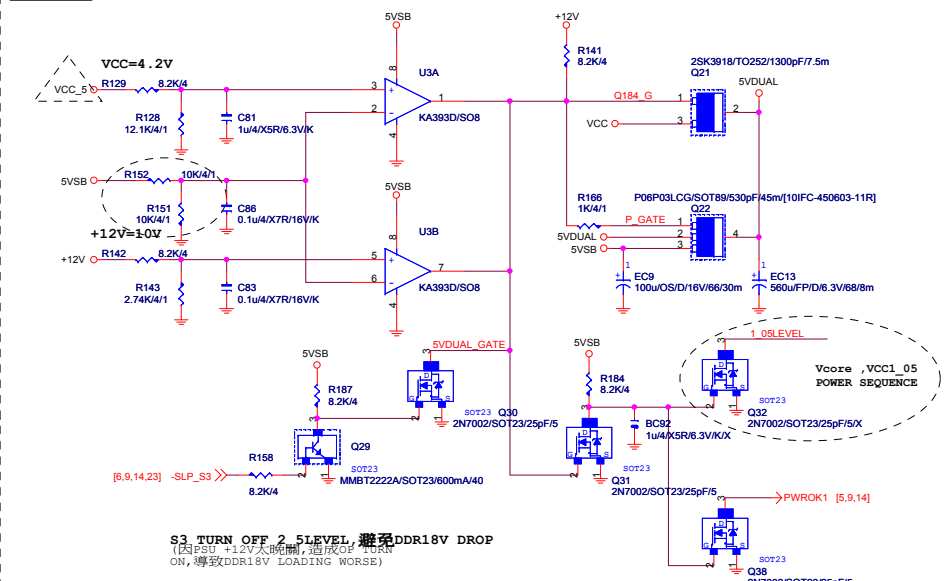
3VDUAL CIRCUIT



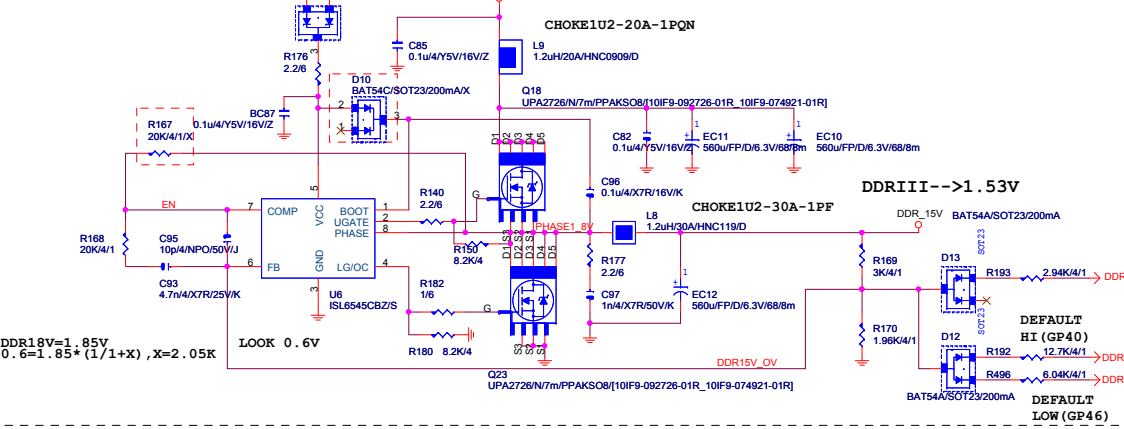
DDRVTT



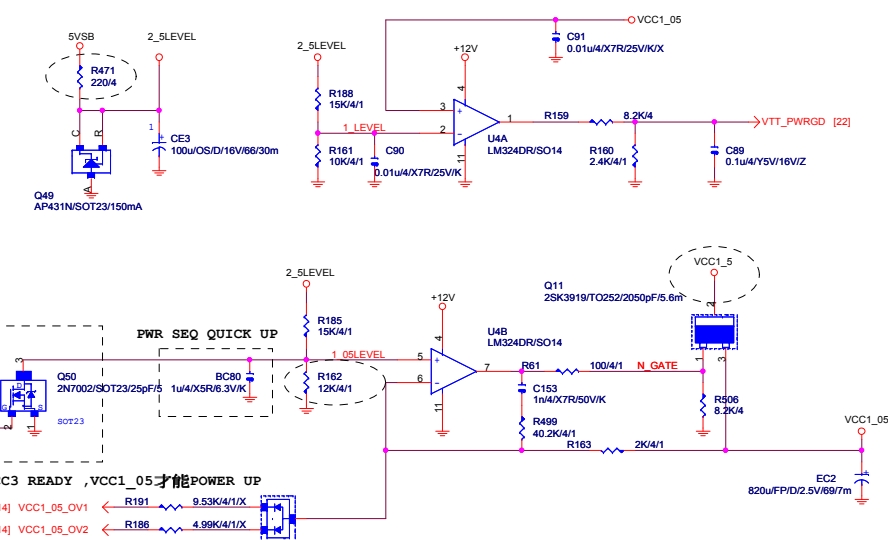
5VDUAL



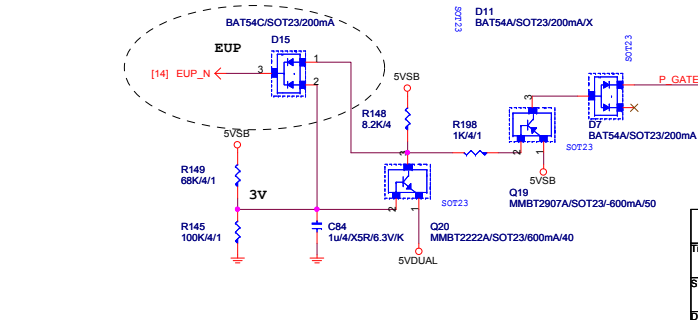
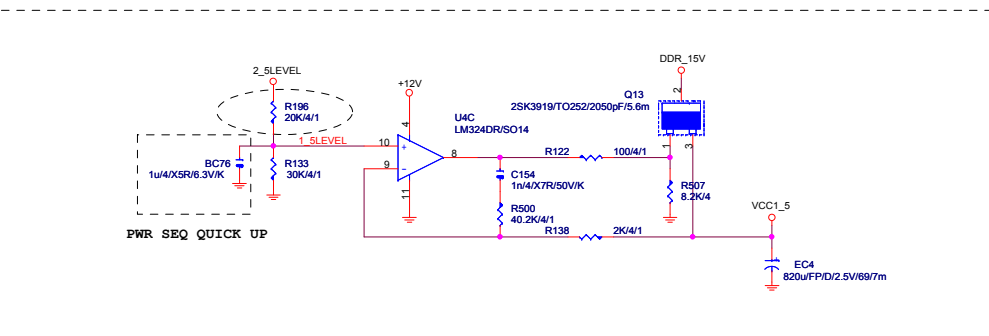
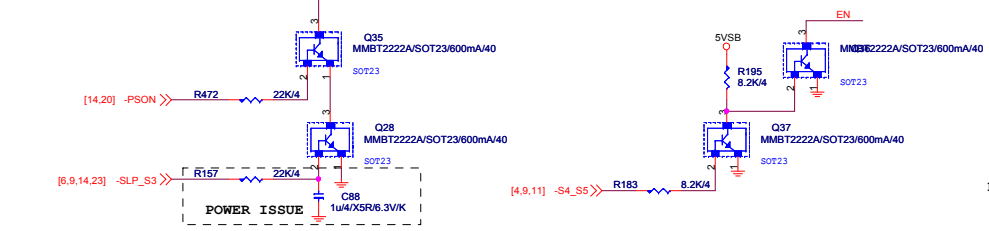
DDR18V



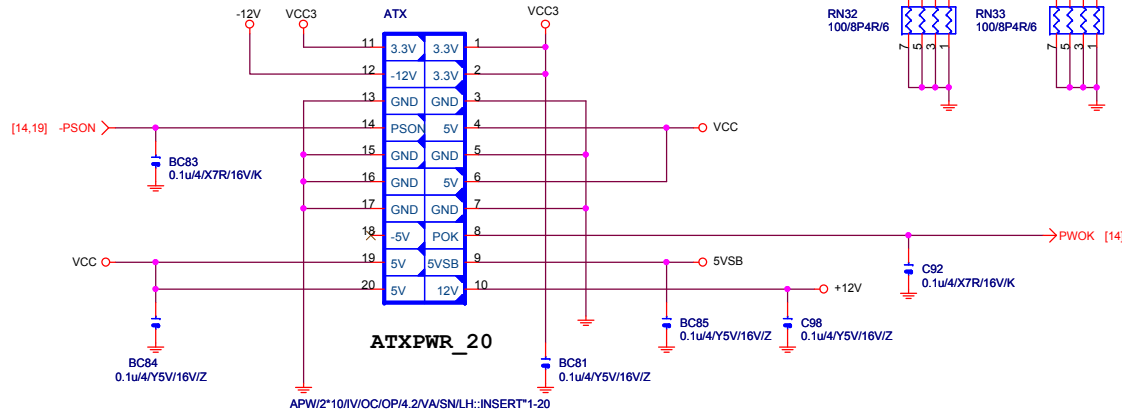
POWER



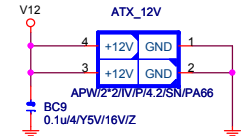
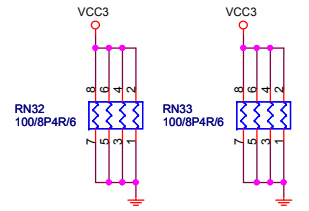
POWER SEQUENCY



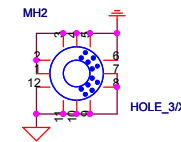
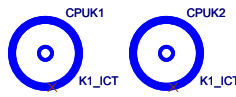
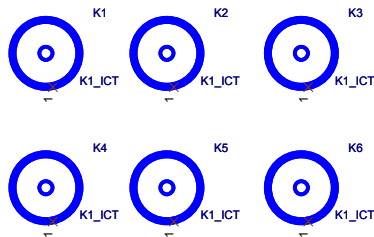
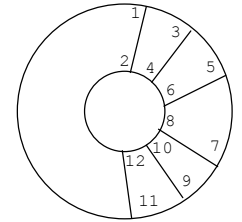
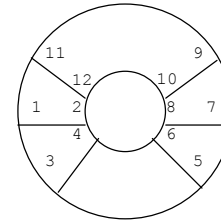
ATX POWER CONNECTOR



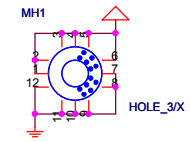
FIX PWR AcBel (ATX-400C-A2ADB)



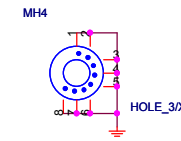
ATX_4-4



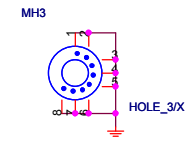
HOLE_4-RH-5MM-1



HOLE_4-RH-5MM-1

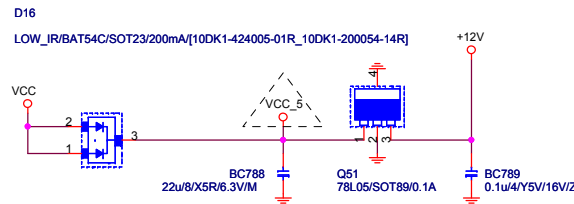


HOLE_4-RH-5MM-5PIN-1



HOLE_4-RH-5MM-5PIN-1

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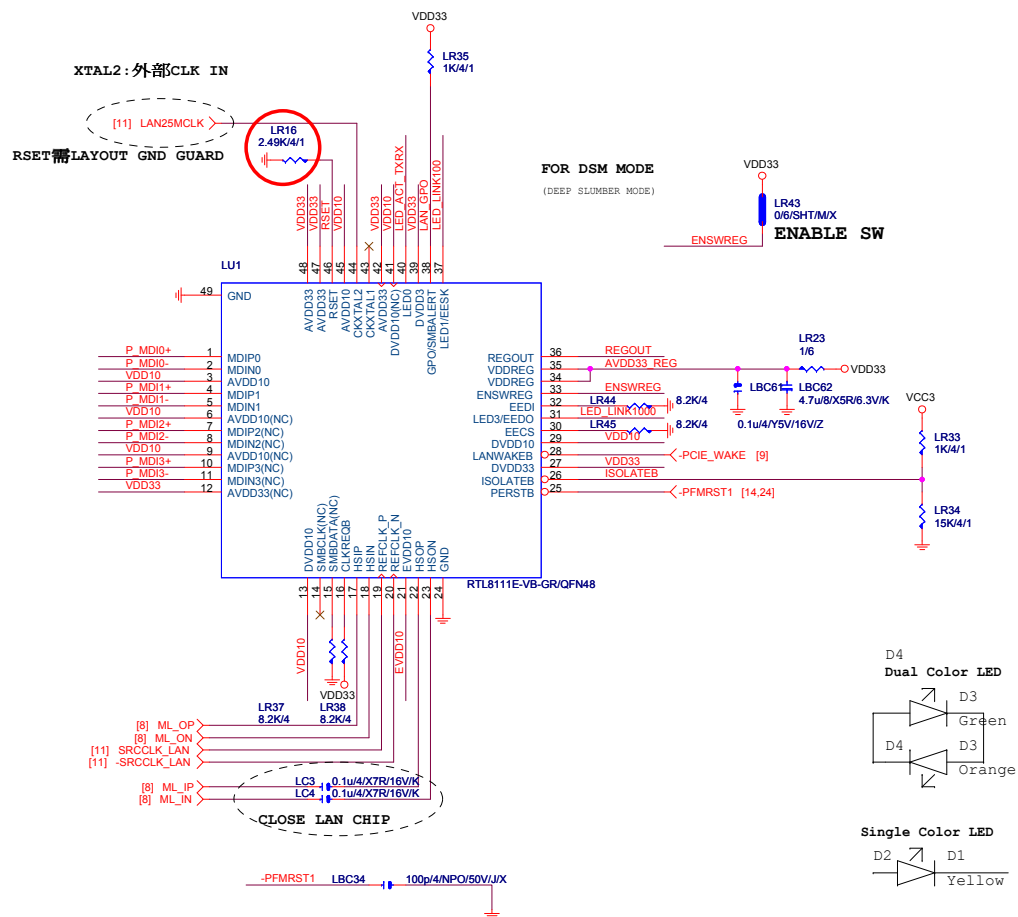


FIX POWER SUPPLY MIN LOAD +5V ISSUE

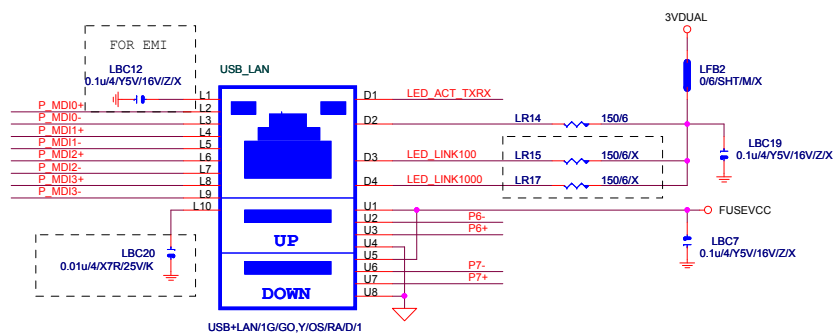
Gigabyte Technology

Title			ATX POWER CONNECTOR
Size B	Document Number	GA-D525TUD	
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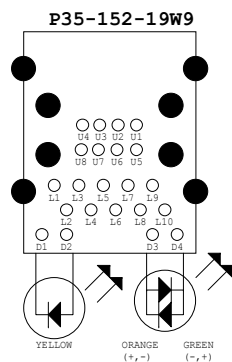
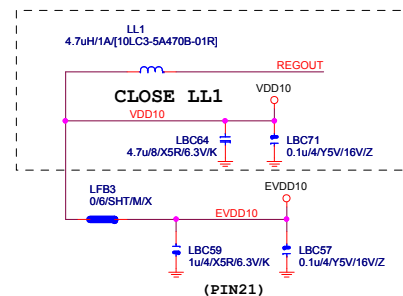
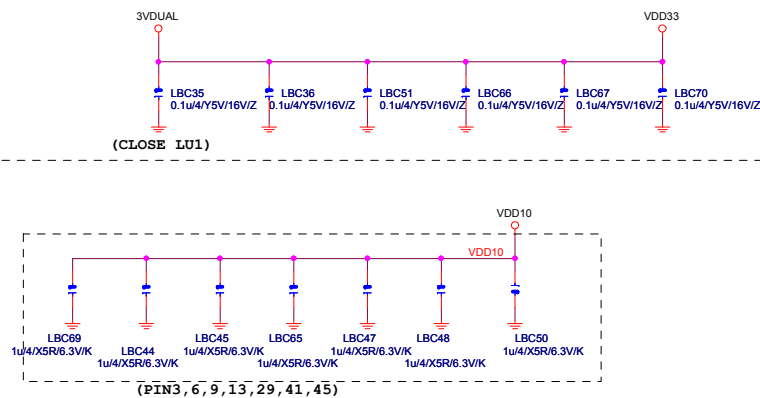
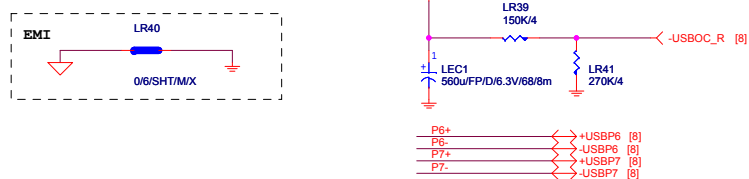
PCIE-1G LAN



USB_LAN CONNECTOR



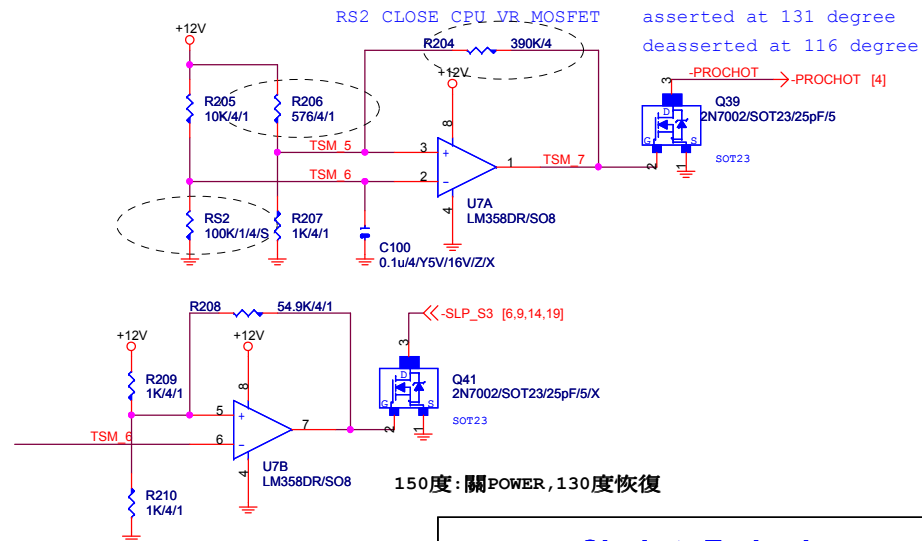
USB LAN




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MPD- : (GPIO25--VCCSUS3+HI+HI+DEFINED(C3/C4/S/1/S3/S/4/S5))-->INTEL
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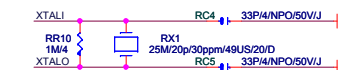
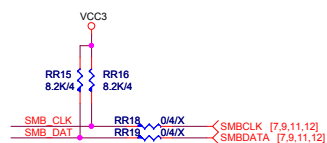
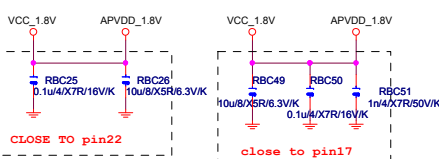
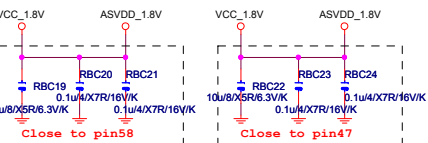
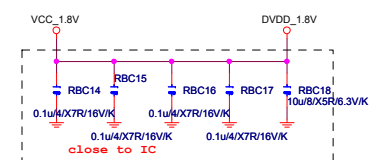
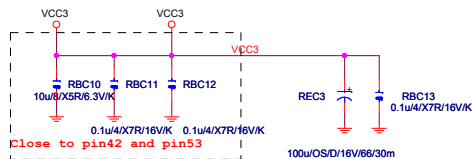
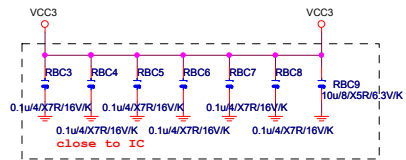


CPU SMART FAN	SMART FAN
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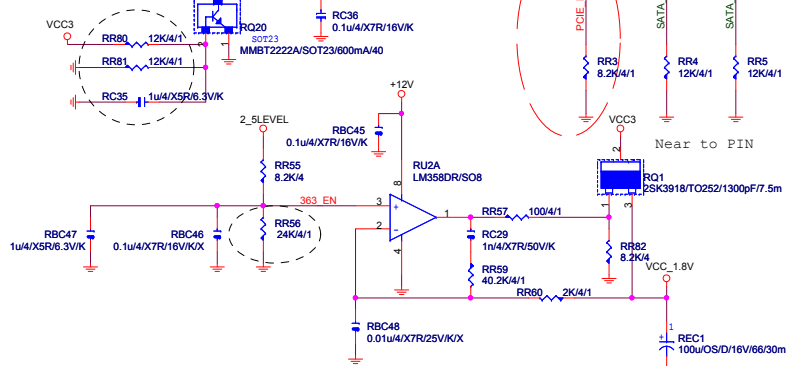
Gigabyte Technology

Title			
FRONT PANEL			
Size B	Document Number	GA-D525TUD	Rev 1.3
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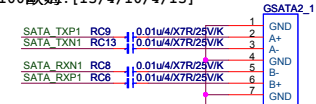


Patch VCC3 Before VCC 1.8V 4ms

6ms delay time



100歐姆: [15/4/10/4/15]



SATA Connector

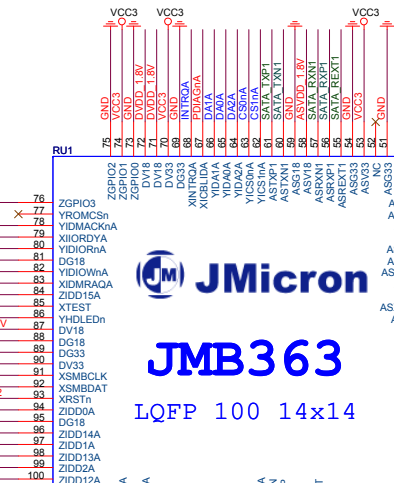
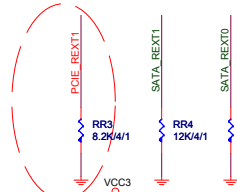
WHITE

SATA27/WH/HOPVA/D1/B

WHITE

SATA27/WH/HOPVA/D1/B

FOR J368 12K-->0.06K/4/1

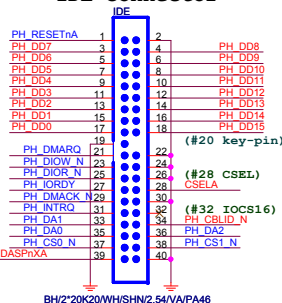


JMicron

JMB363

LQFP 100 14x14

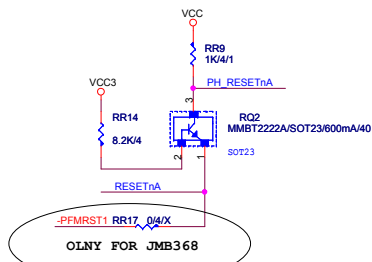
IDE Connector



BH220K20VH/SHN2.54V/A/PA46

80歐姆: [15/5/5/5/15]

SRCLK 50歐姆: [18/4/10/4/18]



OLN FOR JMB368

PH IORDY RR7 1K4/1

CSELA RR8 0/4/SHT/M/X

PH_DMARQ RR11 8.2K/4

PH_INTRQ RR12 8.2K/4

DD7A RR13 8.2K/4

PH_DD7 DD7A

PH_DD8 DD8A

PH_DD9 DD9A

PH_DD5 DD5A

PH_DD6 DD6A

PH_DD10 DD10A

PH_DD11 DD11A

PH_DD3 DD3A

PH_DD12 DD12A

PH_DD2 DD2A

PH_DD13 DD13A

PH_DIOW_N DIOWnA

PH_DIOR_N DIORnA

PH_DMACK_N DMACKnA

PH_DA1 DA1A

PH_DA0 DA0A

PH_CS0_N CS0nA

PH_DA2 DA2A

PH_CS1_N CS1nA

PH_IORDY IORDYA

PH_DMARQ DMARQA

PH_INTRQ INTRQA

PH_CBLID_N PDIAGnA

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